

<b>INFORMATION DISCLOSURE CITATION IN AN APPLICATION</b>  <b>(PTO-1449)</b>				ATTY. DOCKET NO. <b>43876-112</b>		SERIAL NO. <b>09/377,182</b>	
				APPLICANT <b>Craig C. HANSEN, et al.</b>			
				FILING DATE <b>August 19, 1999</b>		GROUP <b>2787</b>	
<b>U.S. PATENT DOCUMENTS</b>							
EXAMINER'S INITIALS	PATENT NO.	DATE	NAME	CLASS	SUBCLASS	FILING DATE	
	4,893,267	1/9/1990	Alsup et al.	364	745		
	4,975,868	12/4/1990	Freerksen	364	748		
	5,157,388	10/20/1992	Kohn	340	800		
	5,201,056	4/6/1993	Daniel et al.	395	800		
	5,268,855	12/7/1993	Mason et al.	364	748		
	5,268,995	12/7/1993	Diefendorff et al.	395	122		
	5,423,051	6/6/1995	Fuller et al.	395	800		
	5,426,600	6/20/1995	Nakagawa et al.	364	764		
	5,592,405	1/7/1997	Gove et al.	364	749		
	5,642,306	6/24/1997	Mennemeier et al.	364	757		
	5,666,298	9/9/1997	Peleg et al.	364	715.08		
	5,669,010	9/16/1997	Duluk, Jr.	395	800.22		
	5,673,407	9/30/1997	Poland et al.	395	725		
	5,675,526	10/7/1997	Peleg et al.	364	754		
	5,680,338	10/21/1997	Agarwal et al.	364	736		
	5,721,892	2/24/1998	Peleg et al.	395	562		
	5,734,874	3/31/1998	Van Hook et al.	395	513		
	5,758,176	5/26/1998	Agarwal et al.	395	800		
<b>FOREIGN PATENT DOCUMENTS</b>							
EXAMINER'S INITIALS	PATENT NO.	DATE	COUNTRY	CLASS	SUBCLASS	Translation	
						Yes	No
<b>OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)</b>							
	K. Diefendorff et al. "Organization of the Motorola 88110 Superscalar RISC Microprocessor" IEEE Micro. April 1992. 40-63.						
	L. Gwennap. "IBM Regains Performance Lead with Power2" Microprocessor Report. October 4, 1993. Vol. 7. No. 13. 1,6-10.						
	L. Gwennap. "IBM Creates Power PC Processors for AS/400" Microprocessor Report. July 31, 1995. 15-16.						
EXAMINER <b>D. H. Malzahn</b>				DATE CONSIDERED <b>3/3/03</b>			

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**INFORMATION DISCLOSURE  
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APPLICATION**

(PTO-1449)

ATTY. DOCKET NO.  
**43876-112**

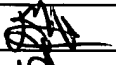




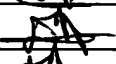

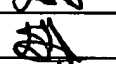



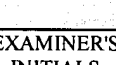
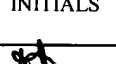

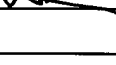
SERIAL NO.  
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APPLICANT  
**Craig C. HANSEN, et al.**

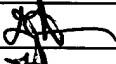

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**2787**



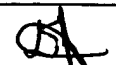
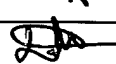

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EXAMINER'S INITIALS	PATENT NO.	DATE	NAME	CLASS	SUBCLASS	FILING DATE
	5,557,724	9/17/1996	Sampat et al.	395	157	
	5,588,152	12/24/1996	Dapp et al.	395	808	
	5,640,543	6/17/1997	Farrell et al.	395	502	
	5,757,432	5/26/1998	Dulong et al.	348	384	
	5,802,336	9/1/1998	Peleg et al.	395	376	
	5,809,292	9/15/1998	Wilkinson et al.	395	563	
	5,818,739	10/6/1998	Peleg et al.	364	715.08	
	5,825,677	10/20/1998	Agarwal et al.	364	726.03	
	5,835,782	11/10/1998	Lin et al.	395	800.42	
	5,886,732	3/23/1999	Humpleman	348	10	
	5,922,066	7/13/1999	Cho et al.	712	204	
	5,983,257	11/9/1999	Dulong et al.	708	603	
	6,016,538	1/18/2000	Gutttag et al.	712	32	
	6,092,094	7/18/2000	Ireton	708	706	
	6,401,194 B1	6/4/2002	Nguyen et al.	712	210	

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	0474246 A2	9/6/1991	EP	—	—		
	0654733 A1	7/5/1994	EP	—	—		

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	L. Kohn et al. "The Visual Instruction Set (VIS) in UltraSPARC" IEEE. 1995. 462-469.
	D. Shaver. "A General-Purpose Array Processor for Seismic Processing" (Nov - Dece 1984) January - March 1998. 15th Anniversary Issue. 5-26.
	R. Lee. "Accelerating Multimedia with Enhanced Microprocessors" IEEE Micro. April 1995. 22-32.
	N. Margulis. "i860 Microprocessor Architecture" 1990. 8-10, 171-175, 182-183.
	A. Levinthal et al. "Parallel Computers for Graphics Applications" 1987. 193-198.

EXAMINER <b>D. H. Malzahn</b>	DATE CONSIDERED <b>3/3/03</b>
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EXAMINER'S INITIALS	PATENT NO.	DATE	NAME	CLASS	SUBCLASS	FILING DATE
	4,025,772	5/24/1977	Constant	235	156	
	4,489,393	12/18/1984	Kawahara et al.	364	728	
	4,701,875	10/20/1987	Konishi et al.	364	728	
	4,727,505	2/23/1988	Konishi et al.	364	728	

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